

Notice of References Cited	Application/Control No. 09/691,083	Applicant(s)/Patent Under Reexamination BODEN ET AL.	
	Examiner George Fourson	Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2003/0040159	02-2003	Sasaki, Katsuhito	438/286
	B	US-2003/0232475	12-2003	Sasaki, Katsuhito	438/303
	C	US-2005/0106791	05-2005	You et al.	438/197
	D	US-2005/0106825	05-2005	You et al.	438/301
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
X	U	Wolf, S. et al, "Silicon Processing for the VLSI Era: Vol.1, Process Technology", Lattice Press, 1986, pp.207-210
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Copy previously supplied (corrected citation)